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St. JOSEPH'S COLLEGE OF ENGINEERING
 (An Autonomous Institution)
St. Joseph's Group of Institutions
 OMR, CHENNAI - 119



APRIL 2026

DEPARTMENT OF ARTIFICIAL INTELLIGENCE AND DATA SCIENCE

S.No.	Title of the Events and Photographs	Details of the Event
	STUDENT ACHIEVEMENTS/COMPETITION ATTENDED BY STUDENTS Conferences > 2026 8th International Confer... Real-Time Bidirectional Sign Language Translator with Gesture Recognition and Virtual Avatar Publisher: IEEE Cite This PDF Kaniga M; Janani R; Sivaprabha T All Authors Abstract Document Sections I. Introduction Abstract: The communication gap between hearing and deaf or hard-of-hearing individuals still creates major obstacles to participation in workplaces, classrooms, and everyday social settings. This paper offers a Real-Time Bidirectional Sign Language translator that overcomes this challenge by combining a hybrid deep-learning framework that integrates CNN, BiLSTM, and Transformer layers for gesture detection with a 3D	<i>Student Kaniga M, Janani R, and staff Sivaprabha T have successfully published their IEEE paper titled “Real-Time Bidirectional Sign Language Translator with Gesture Recognition and Virtual Avatar.” This research focuses on bridging the communication gap between hearing individuals and deaf or hard-of-hearing people by developing an advanced real-time sign language translation system. The paper integrates deep learning techniques such as CNN, BiLSTM, and Transformer layers for accurate gesture recognition along with a 3D virtual avatar for effective communication.</i>

SmartQueryViz: An AI-Driven MongoDB Query Generator and Visualizer for Sensor-Based Applications

Publisher: IEEE

Cite This



[S Velvizhi](#); [S Sahana](#); [M.E Sivaprabha](#) All Authors



Abstract

Abstract:

Document
Sections

The applications based on sensors produce enormous data of different kinds and scales. In this case, effective query and data retrieval also become critical. In the above scenario, the formation of queries in a NoSQL database like MongoDB also demands certain technical knowledge about the syntax of the former and, thus, is beyond the capabilities and reach of non-technical persons. In this paper, the authors



I. Introduction

Dual Age Estimation Framework Using PINN and Modified AlexNet with Grad-CAM Visualization: A Deep Learning Approach for Chronological and Biological(Apparent) Age Prediction

Publisher: IEEE

Cite This



[Divilin Sweety D S](#); [Abinaya K M](#); [Ananthi S](#) All Authors



Students *S Velvizhi, S Sahana*, and faculty *Sivaprabha T* have successfully published their IEEE paper titled “*SmartQueryViz: An AI-Driven MongoDB Query Generator and Visualizer for Sensor-Based Applications.*” The research focuses on improving data retrieval and query generation in sensor-based applications by using artificial intelligence to simplify MongoDB query formation.

Students *Divilin Sweety D S* and *Abhinaya K M*, along with faculty member *S. Ananthi*, have successfully published a research paper titled “*Dual Age Estimation Framework Using PINN and Modified AlexNet with Grad-CAM Visualization: A*

RESEARCH ARTICLE | FEBRUARY 27 2026

Multi-class colon disease detection through image segmentation using deep learning 🛒

S. Sanjiv ✉; S. Nandha; S. Ananthi

+ Author & Article Information

AIP Conf. Proc. 3383, 020032 (2026)

<https://doi.org/10.1063/5.0308604>

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 Tools ▾

This project presents a pioneering approach to colon disease prophecy and surgical help by integrating leading broad-cast analytics, sign control, and electronic technology. Leveraging deep education models like EfficientNet and ResNet, bureaucracy accurately labels the colon from healing imaging dossiers. Through the unification of MediaPipe, doctors can intuitively control the program connection using help gestures, simplifying a seamless and shared happening. The project's image deals with algorithms that re-solve tissue insight inside the colon, offering important judgments for disease indicators and surgical preparation. An intelligent machine arm

Deep Learning Approach for Chronological and Biological (Apparent) Age Prediction” in an IEEE conference. The study presents an innovative deep learning framework that integrates PINN and a modified AlexNet model to accurately estimate both chronological and biological age. By incorporating Grad-CAM visualization, the research enhances model interpretability, offering insights into facial feature analysis.

Students S. Sanjiv and S. Nandha, along with faculty member S. Ananthi, have successfully published a research article titled “Multi-class Colon Disease Detection through Image Segmentation using Deep Learning” in AIP Conference Proceedings. The study presents an innovative approach to colon disease detection by leveraging advanced deep learning models such as EfficientNet and ResNet for accurate image segmentation. The proposed system enhances diagnostic precision by effectively analyzing medical imaging data and identifying multiple disease classes.

Application Details	
APPLICATION NUMBER	202641047062
APPLICATION TYPE	ORDINARY APPLICATION
DATE OF FILING	13/04/2026
APPLICANT NAME	1. GIFTLIN C 2. DEEPIKA M 3. KUMARI DEEPIKA P
TITLE OF INVENTION	AUTONOMOUS SELF HEALING CODEBASE SYSTEM USING MULTI AGENT FEEDBACK AND ADAPTIVE TEST VALIDATION
FIELD OF INVENTION	COMPUTER SCIENCE
E-MAIL (As Per Record)	steffinagiftlin09@gmail.com
ADDITIONAL-EMAIL (As Per Record)	giffy1109@gmail.com
E-MAIL (UPDATED Online)	
PRIORITY DATE	
REQUEST FOR EXAMINATION DATE	--
PUBLICATION DATE (U/S 11A)	24/04/2026

Application Status	
APPLICATION STATUS	Awaiting Request for Examination
View Documents	
→ Filed → Published → RQ Filed → Under Examination → Disposed	

The students Giftlin C, Deepika M, and Ms Staff Kumari Deepika P have successfully filed and published a patent titled “Autonomous Self-Healing Codebase System Using Multi-Agent Feedback and Adaptive Test Validation” in the field of Computer Science. The patent application (No. 202641047062) was filed on 13 April 2026 and subsequently published on 24 April 2026.

STAFF NPTEL CERTIFICATION/ STAFF ACHIEVEMENTS



Dr Vijayalakshmi Janakiraman has been awarded a Certificate of Reviewing by the journal Engineering Applications of Artificial Intelligence in recognition of her valuable contribution as a reviewer. This certificate acknowledges her completion of one review in April 2026, highlighting her commitment to academic excellence and scholarly peer review. The recognition was conferred by the Editors of the journal, appreciating her efforts in maintaining the quality and integrity of published research.



Ms Ananthi S has been recognized as a reviewer for the Journal of Engineering Research and Sciences (JENRS) in the domain of Artificial Intelligence and Computer Science (AIC). This selection reflects a careful evaluation of her research expertise, academic contributions, and scientific perspective. As a reviewer, she will contribute to maintaining the quality and integrity of scholarly publications by providing critical insights and constructive feedback on research manuscripts.



Dr. J. VENU GOPALA KRISHNAN, M.Tech., E.L.E., Ph.D.,
PRINCIPAL

Date: 13.04.2026

To

Dr. R. Ramya
Associate Professor
Department of Artificial Intelligence and Data Science
St. Joseph College of Engineering
Chennai.

CERTIFICATE OF APPRECIATION

We wish to thank you for the invaluable contribution to the FDP on "AI, IoT and LLM Innovations for Modern Engineering and Education", held online on 10th, April, 2026.

We appreciate the time you have contributed out of your busy schedule for joining us to equip our faculty members with your sessions. From the feedback received, the sessions were informative and valuable. Your willingness to volunteer your time, energy and support are greatly appreciated

With Regards,

[Signature]
PRINCIPAL
Jeppiaar Institute of Technology
Kunnam, Sunguvarchattam - 631 604,
Chennai, Tamil Nadu - 605 006



Ref. No.: CLGIET/FDP/2026/05

Date: 20th April 2026

APPRECIATION LETTER

To

D. Evangeline Nesa Priya
Assistant Professor, Artificial Intelligence and Data Science
St Joseph's College of Engineering

Subject: Appreciation for Delivering Expert Session in FDP.

Dear Sir/Madam,

On behalf of CLG Institute of Engineering and Technology Sumerpur, Pali, Rajasthan, it gives us immense pleasure to express our sincere appreciation for your valuable contribution as a Resource Person in the Five-Day National Level Faculty Development Programme (FDP) on "Advances in Research and Technologies in Education", conducted in online mode.

We would like to specially acknowledge your expert session delivered on 19th April 2026, which was highly informative, insightful, and engaging. The depth of knowledge and clarity with which you presented the topic greatly benefited all the participants. Your interaction with the participants and willingness to share your expertise is truly commendable.

We deeply appreciate your time, effort, and dedication in making this FDP a grand success. Your contribution has been highly valuable and enriched the academic and professional growth of our faculty members.

Dr. R. Ramya, Associate Professor in the Department of Artificial Intelligence and Data Science at St. Joseph's College of Engineering, Chennai, served as a Resource Person for the Faculty Development Programme on "AI, IoT and LLM Innovations for Modern Engineering and Education" held on 10th April 2026 at Jeppiaar Institute of Technology. She delivered insightful sessions that were highly informative and valuable for the participating faculty members.

Ms D. Evangeline Nesa Priya, Assistant Professor in Artificial Intelligence and Data Science at St. Joseph's College of



Engineering, was honored with an appreciation from CLG Institute of Engineering and Technology, Sumerpur, Rajasthan, for her valuable contribution as a Resource Person in the Five-Day National Level Faculty Development Programme (FDP) on “Advances in Research and Technologies in Education,” conducted in online mode. She delivered an expert session on 19th April 2026

Dr. R. Ramya has successfully completed the “Python for Data Science” course through the NPTEL Online Certification program conducted during January–February 2026. This 4-week course focuses on developing strong foundational skills in data science using Python. She achieved an impressive consolidated score of 78%, securing a perfect 25/25 in online assignments and 52.5/75 in the proctored examination.



Elite
NPTEL ONLINE CERTIFICATION
 (Funded by the MoE, Govt. of India)



This certificate is awarded to
VIJAYALAKSHMI J
 for successfully completing the course
Python for Data Science



with a consolidated score of **86** %

Online Assignments	25/25	Proctored Exam	60.75/75
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Total number of candidates certified in this course: **16401**


Prof. Andrew Thangaraj
Chair
Centre for Outreach and Digital Education, IITM

Jan-Feb 2026
(4 week course)


Prof. Vignesh Muthuvijayan
NPTEL Coordinator
IIT Madras


Indian Institute of Technology Madras



Roll No: NPTEL26CS80S180404908
To verify the certificate 
No. of credits recommended: 2

Dr Vijayalakshmi J has successfully completed the prestigious NPTEL Online Certification course on Python for Data Science, conducted by the Indian Institute of Technology Madras under the SWAYAM initiative of the Government of India. Demonstrating outstanding academic excellence, she secured an impressive consolidated score of 86%, achieving a perfect 25/25 in online assignments and 60.75/75 in the proctored examination. Notably, she earned a place among the top 1% of performers obtaining Elite + Silver , standing out among 16,401 certified candidates.

Dr Vijayalakshmi J has successfully completed the NPTEL Online Certification course on Data Base Management System, offered by the Indian Institute of Technology Kharagpur under the SWAYAM initiative of the Government of India. She secured a



commendable consolidated score of 78%, with 23.54/25 in online assignments and 54/75 in the proctored examination. Her performance placed her among the top 5% of candidates got Elite + Silver distinguishing her among 12,988 certified participants.

Dr. Sherly Pushpa Annabel, Professor from the Department of Artificial Intelligence and Data Science, has been honored with a total reward of Rs. 69,000 in recognition of her outstanding contributions in areas such as Publications (55000), Patents(4000) and Ph.D. Registration(10000). This recognition, awarded for the academic year 2025–2026, highlights her exceptional dedication, academic excellence, and significant contributions to research and institutional development, which are highly appreciated by the institution.

Dr Raja Kumar M P, Professor from the Department of Artificial Intelligence and Data Science, has been honored with a total



reward of Rs. 1,57,000 in recognition of his outstanding contributions in areas such as Publications (1,53,000) and Patents(4000). Presented for the academic year 2025–2026.

Dr. Kootteeswaran R, Associate Professor, has been honored with a reward of Rs. 70,000 in recognition of his outstanding contributions in areas such as Publications (68000) and Mentoring Students (2000). Awarded for the academic year 2025–2026, this recognition reflects his dedicated efforts, strong academic performance, and meaningful contributions to research and institutional development, for which he is sincerely appreciated.

Dr Ramya , Associate Professor from the Department of Artificial Intelligence and Data Science, has been honored with a total reward of Rs. 63,000 in recognition of her outstanding contributions in areas such as Publications (59,000) and Patents(4000). Conferred for the academic year 2025–2026, this honor recognizes her sustained dedication, notable academic accomplishments, and significant



contributions to advancing research and supporting the institution's growth, for which she is greatly appreciated.

Mr Senthil Kumar D, Assistant Professor, has been honored with a reward of Rs. 38,000 in recognition of his outstanding contributions in areas such as Publications (34000) and Patents (4000). Ms. Jayasri, Assistant Professor, has been awarded a reward of Rs. 35,000 in appreciation of his notable contributions, including Rs. 31,000 for Publications and Rs. 4,000 for Patents, reflecting his active involvement in research and academic advancement.



Ms. Kaviarasi J, Assistant Professor has been rewarded Rs. 24,000 (publication 20000 and patent 4000) for her



contributions to academic and research activities. Mr. Vijay has received Rs. 18,000 (publication 14000 and patent 4000) in recognition of his involvement in publications and related initiatives; these awards, presented for the academic year 2025–2026, reflect their dedication and valuable support to the institution's academic progress.

Dr. Nisha A S, Associate Professor and Dr. Vijayalakshmi, Associate Professor have each been rewarded Rs. 19,000 in recognition of their contributions to Publications (Rs. 15,000) and Patents (Rs. 4,000), reflecting their active involvement in research and academic excellence. Awarded for the academic year 2025–2026, this recognition highlights their dedication and consistent efforts toward advancing research and supporting the institution's growth.



Ms Sathya, Assistant Professor and Ms Ananthi, Assistant Professor have each been awarded Rs. 8000 in recognition of their contributions to Paper Publications indicating their proactive participation in research and knowledge advancement. Awarded for the academic year 2025–2026, this recognition highlights their dedication and consistent efforts toward advancing research and supporting the institution's growth.



Ms. Kumari Deepika and Mr. Durai Raji were awarded a reward of Rs. 4,000 in recognition of their valuable contribution to patent publication. Their achievement highlights their dedication to research and innovation, bringing pride to the institution. This accomplishment reflects their commitment to advancing knowledge and contributing meaningfully to academic and technological development.



Dr. Baghia Laxmi R., Ms. Aswini, Ms. Sivaprabha, Ms. Dhana Bhavithra, Dr. Hemalatha, and Ms. Dhiva J. C have been awarded a prize of Rs. 4,000 each in recognition of their valuable contribution to patent publication. This recognition further highlights their proactive involvement in advancing intellectual property and fostering a culture of innovation within the academic community.





Ms. Arunmozhi Kalanchiam, Mr. Sugin, Mr. Vinoth, Ms. Kavitha, Ms. Evangeline Nesa Priya, and Ms. Kalpana have been awarded Rs. 4,000 each in recognition of their valuable contribution to patent publication. Their achievement reflects their dedication to research and innovation, contributing significantly to the institution's academic excellence.



Conferences > 2025 2nd Global AI Summit - I... ?

HARE-Net: Hybrid Attention Residual Ensemble Networks for Robust Plant Disease Detection based on Image-Processing and Machine Learning

Publisher: IEEE

[Cite This](#)



[Komal Mishra](#) ; [Deepak Juneja](#) ; [D. Deepa](#) ; [Megha Dabas](#) ; [T.V.Hyma Lakshmi](#) ; [Subodh Bansal](#)

All Authors



Abstract

Document
Sections



Introduction

Abstract:

The topic of plant diseases should not be overlooked as everyday losses in food production due to diseases result in great yield as well as economic losses.

Traditional pathogen diagnosis is imprecise, labor-consuming and available to smallholder farmers in few locations. Automated classification of plant diseases

authored with Umamageswari B., Bala Gayathri D., and Malchijah Raj M. S., explores advanced deep learning approaches for detecting phishing URLs and protecting users from cyber threats. It highlights the effectiveness of the semi-supervised GAN-BERT model in enhancing the accuracy of phishing detection systems.

Ms D. Deepa, along with a team of researchers, has published a high-impact paper titled "HARE-Net: Hybrid Attention Residual Ensemble Networks for Robust Plant Disease Detection based on Image-Processing and Machine Learning" through IEEE. Published as part of the 2025 2nd Global AI Summit, this research introduces a sophisticated ensemble network designed to overcome the limitations of traditional pathogen diagnosis, which is often imprecise and labor-intensive.

A Hybrid Deep Learning Guided Few-Shot Biomedical Image Classification for Resource Constrained Clinical Settings

Publisher: IEEE

Cite This



[A Rajalakshmi](#); [S Ananthi](#); [R K Padmashini](#); [Jayashree N](#); [M D Boomija](#); [Prasannambiga](#) [G](#) All Authors



Abstract

Document
Sections



Introduction

Abstract:

The insufficient annotated data, great variability of modalities, and the computational requirements of standard deep learning models are a strong impediment to biomedical image classification in low-resource clinical environments. This paper proposes a hybrid few-shot learning model, a CNN-

Ms S. Ananthi has successfully published a research paper titled “Deep Learning-Based EEG Signal Classification for Enhancing Brain-Computer Interface Accuracy” in an IEEE conference, highlighting innovative advancements in brain-computer interface systems using deep learning techniques. The study focuses on improving the accuracy and efficiency of EEG signal interpretation through advanced models such as Convolutional Neural Networks. This research contributes significantly to real-time communication between the human brain and external devices, paving the way for improved assistive technologies.

Ms S. Ananthi has successfully published a research paper titled “A Hybrid Deep Learning Guided Few-Shot Biomedical Image Classification for Resource Constrained Clinical Settings” in an IEEE International Conference. The research focuses on addressing challenges in

Deep Learning-Based EEG Signal Classification for Enhancing Brain-Computer Interface Accuracy

Publisher: IEEE

Cite This

PDF

Yogendra Narayan ; S. Ananthi ; Sardor Khujamgulov ; Harpreet Kaur Thind ; Deepak Juneja ; Ramandeep Kaur

All Authors



Abstract

Abstract:

Document
Sections

Brain Computer Interface (BCI) systems are one of the revolutionary technologies that are currently offering an actual communication between the human brain and the external device that largely depends on the interpretation of the Electroencephalogram (EEG) signals. The present paper has proposed an EEG classification model using deep learning that would assist in enhancing accuracy and real-time operation of BCI. It used Convolutional Neural Networks



Literature

(12) PATENT APPLICATION PUBLICATION

(21) Application No.202641035098 A

(10) INDIA

(43) Publication Date : 03/04/2026

(22) Date of filing of Application : 23/03/2026

(54) Title of the invention : ARTIFICIAL INTELLIGENCE-BASED SHORTEST PATH OPTIMIZATION SYSTEM FOR ELECTRIC VEHICLES IN IOT-ENABLED SMART CITIES TO REDUCE THE ENERGY CONSUMPTION

(71) Name of Applicant :

1) Dr. A. Gopinath
Address of Applicant : Assistant Professor and Head,
Department of Electronics with Artificial Intelligence,
Dharmamurthi Rao Bahadur Calavala Cunnan Chetty's Hindu
College, Pattabiram, Chennai, 600072 Chennai Tamil Nadu India

2) Dr. S. Senthamaraselvi

3) Dr. V. Shanmuchi

4) S. Dharanika

5) Honddli Silva Hari Prasad

6) Dr. Pinky Choushan

7) P. Nirmla

8) Neeraja B

9) Sonali R. Nandanwar

10) R. Gobi

11) Keerthana G

12) Pavan Prabhakar Vitpute

(72) Name of Inventor :

1) Dr. A. Gopinath

2) Dr. S. Senthamaraselvi

3) Dr. V. Shanmuchi

4) S. Dharanika

5) Honddli Silva Hari Prasad

6) Dr. Pinky Choushan

7) P. Nirmla

8) Neeraja B

9) Sonali R. Nandanwar

10) R. Gobi

11) Keerthana G

12) Pavan Prabhakar Vitpute

(57) Abstract :

Artificial Intelligence-Based Shortest Path Optimization System for Electric Vehicles in IoT-Enabled Smart Cities is the proposed invention. The proposed invention leverages real-time data collected from interconnected sensors, traffic monitoring units, and vehicle systems to assess dynamic factors such as traffic density, road conditions, charging station availability, and energy usage patterns. Advanced AI algorithms like Generative Adversarial Networks (GAN), including optimization and predictive models like gradient descent, process this data to determine the most energy-efficient routes rather than simply the shortest distance. By continuously adapting to changing urban conditions, the system provides intelligent route recommendations to electric vehicles, minimizing power consumption and travel time. The integration of IoT infrastructure ensures seamless communication between vehicles and city systems, enabling efficient decision-making. This approach contributes to improved energy management, reduced operational costs, and enhanced sustainability in smart transportation networks.

No. of Pages : 14 No. of Claims : 4

biomedical image classification, particularly in scenarios with limited annotated data and computational resources. It proposes an innovative hybrid few-shot learning approach that enhances classification accuracy even with minimal data availability.

Ms S Dharanika has published a patent titled “Artificial Intelligence-Based Shortest Path Optimization System for Electric Vehicles in IoT-Enabled Smart Cities to Reduce Energy Consumption.” The patent was filed on 23 March 2026 and published on 03 April 2026 in the Indian Patent Office Journal. This invention focuses on optimizing energy-efficient routes for electric vehicles using artificial intelligence

Ms S Dharanika has published a patent titled “Hybrid Machine Learning-IoT-

	(12) PATENT APPLICATION PUBLICATION (19) INDIA (22) Date of filing of Application :23/03/2026 (21) Application No.202641035120 A (43) Publication Date : 03/04/2026 (54) Title of the invention : HYBRID MACHINE LEARNING-IOT-DRONE FRAMEWORK FOR REAL-TIME EARTHQUAKE PREDICTION AND INTELLIGENT EARLY WARNING SYSTEM (51) International classification (31) Priority Document No (32) Priority Date (33) Name of priority country (86) International Application No (87) International Publication No (61) Patent of Addition to Application Number (62) Divisional to Application Number (71)Name of Applicant : 1)B.DivyaJyothi Address of Applicant :Assistant Professor, Department of CSE, CVR College of Engineering, Hyderabad, 501510 Hyderabad Telangana India 2)Dr. Vikram Madhukar Agone 3)Irfana I 4)M Laxmi 5)S.Dharanika 6)Vipul Dubey 7)S.Archanadevi 8)Dayananda Sagar Academy of Technology and Management 9)Bharath Kumara J 10)Shanthi P 11)Dr Mohd Umar Farooq 12)Nagasuma Channarayapalna Venugopal (72)Name of Inventor : 1)B.DivyaJyothi 2)Dr. Vikram Madhukar Agone 3)Irfana I 4)M Laxmi 5)S.Dharanika 6)Vipul Dubey 7)S.Archanadevi 8)Bharath Kumara J 9)Shanthi P 10)Dr Mohd Umar Farooq 11)Nagasuma Channarayapalna Venugopal (57) Abstract : Hybrid Machine Learning-IoT-Drone Framework for Real-Time Earthquake Prediction and Intelligent Early Warning System is the proposed invention. The proposed invention utilizes a network of distributed sensors to continuously monitor seismic and environmental parameters, transmitting the collected data to a centralized or edge-based processing unit. Advanced hybrid machine learning models analyze this data to detect hidden patterns and early indicators of seismic disturbances. In addition, drones are deployed to gather supplementary real-time information from affected or high-risk areas, enhancing situational awareness and data accuracy. By integrating multi-source data through efficient fusion techniques, the system generates timely and reliable alerts that are communicated to authorities and communities through digital platforms. The proposed approach aims to minimize the impact of earthquakes by improving prediction accuracy, reducing response time, and supporting effective disaster management strategies. No. of Pages : 14 No. of Claims : 3 The Patent Office Journal No. 14/2026 Dated 03/04/2026 38864	Drone Framework for Real-Time Earthquake Prediction and Intelligent Early Warning System.” The patent was filed on 23 March 2026 and published on 03 April 2026 in the Indian Patent Office Journal. This invention proposes an advanced framework that integrates machine learning Sugin S. V has successfully published a patent titled “Machine Learning-Based Intrusion Detection and Anomaly Detection System for Securing Cloud Networks,” which was officially featured in the Indian Patent Office Journal (No. 14/2026) on 3rd April 2026, following its filing on 24th March 2026. This innovative work focuses on enhancing cloud network security using advanced machine learning techniques
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DEPARTMENT OF ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING

Sl. No.	Photographs Captured During Event	Corresponding remarks in regarding the status of activity execution
1.	Dr. Ancy S Published Patent 	Title of Innovation : Smart Forming System.
2.	Ms. Kiruba Wesley Published Patent	

[illegible]

The Patent Office Journal No. 14/2026 Dated 03-04-2026

28553

Title of Innovation : Smart IoT Powered Earthquake Prediction with Deep Learning

DEPARTMENT OF BIOTECHNOLOGY

S.No.	Title of the Events and Photographs	Details of the Event
1.	STUDENT ACHIEVEMENTS  Letter of Intent (LOI) Superset ID: 690089 Date: April 28, 2026 Dear AMSAPRABA A P, We are immensely glad to extend a hearty welcome to you to our Cognizant family. 1. This LOI refers to your application for employment with Cognizant Technology Solutions India Private Limited ("Cognizant") and subsequent discussions. Basis the information and representation provided by you, we have found you suitable for GenC role, and your designation will be Programmer Analyst Trainee. Upon final scrutiny, a formal full-time employment Offer letter with the compensation, terms and conditions, rules, regulations & policies ("Employment Agreement") will be issued and such Employment Agreement shall be subject to and be effective only upon your successful completion of full-time internship, if offered to you, and background verification. 2. If full-time internship is offered to you, a) You will have an opportunity to do a full-time Internship where you would be deployed into a formal training to a business specific skill track, and it will be used as basis towards your allocation to project/roles and terms and conditions governing such training program shall be detailed in that Offer Letter. b) It is hereby clarified that participation in this full-time Internship program shall not deem to constitute you to be an employee of Cognizant and the scope of this full-time Internship program does not include any supervisory responsibilities and that there is no agency, fiduciary or employer-employee relationship intended or created by reason of this LOI. c) Cognizant shall carry no worker's compensation insurance or any health or accident insurance under this full-time internship program and not be liable to pay any contributions to applicable statutory payments such as workmen compensation, provident fund nor shall provide any other contribution or benefits which might be expected in an employer-employee relationship. d) Cognizant will not encourage any claim with regard to compensation or other statutory payments under this LOI and it is here by clarified that participation in this full-time internship program shall not entitle you for any benefits paid or made available to that of Cognizant employees. 3. Upon successful completion of full-time Internship program and on completion of background verification, you would be onboarded as a full-time employee under a definitive Employment Agreement, as per business needs. 4. If direct full-time employment is offered to you upon your successful completion of background verification, you would be onboarded as a full-time employee under a definitive Employment Agreement, where you should successfully complete the Cognizant identified training program / on the job training. In the event of unsatisfactory performance during this training, Cognizant reserves rights at its sole discretion to terminate full-time employment. 5. Upon successful completion of full-time internship and joining as full-time employee, or directly joining as full-time employee, your annual total cost to company (CTC) shall be INR 416,331 / per annum. The detailed information on compensation and benefits will be provided in your full-time employment Offer Letter. 6. Cognizant has zero tolerance towards malpractice in any form and reserves all rights with respect to hiring decisions and issue of full-time Offer. This document is only a letter of intent and does not constitute any contractual relationship between you and Cognizant. Cognizant holds all rights to withhold or cancel this LOI and/or the Internship Offer Letter. Full-time employment Offer Letter due to non-confirmation of performance benchmark or moral code of conduct. Code of Conduct: You shall comply with Cognizant's Core Values and Standards of Business Conduct, located at and incorporated herein by reference https://www.cognizant.com/us/en/documents/code-of-ethics.pdf. 7. This LOI from Cognizant is valid for 6 calendar days, from the date of the LOI. Hence, you are requested to accept or decline the LOI within 7 calendar days from the date of the LOI. In case we do not receive any response from your end within the aforementioned time period, this LOI shall stand withdrawn, and will be considered as void. Any extension to the LOI validity will be at the sole discretion of Cognizant. Please note that this LOI is not deemed to be considered as an employment contract or offer letter for purposes of joining Cognizant as an employee. For all onboarding formalities, the definitive Employment Agreement is required. 8. Upon accepting this LOI, you hereby consent for Cognizant Technology Solutions India Private Limited to receive the personal information and/or sensitive personal information (Personal Information) submitted by you for background verification and share the same with its engaged vendors located in India for conducting mandatory background checks for employment opportunities with Cognizant. <i>A copy of proof</i>	Our Final-year students (Batch 2022-2026), Amsa Prabha A P and Laura Jean F secured a placement offer as a Programmer Analyst at Cognizant, Chennai with CTC of Rs 4.1 lakhs.



Letter of Intent (LOI)

Supersat ID: 000033

Date: April 28, 2026

Dear Laura Jean F,

We are immensely glad to extend a hearty welcome to you to our Cognizant family.

1. This LOI refers to your application for employment with Cognizant Technology Solutions India Private Limited ("Cognizant") and subsequent discussions. Based on the information and representation provided by you, we have found you suitable for GenC role, and your designation will be **Programmer Analyst Trainee**. Upon final scrutiny, a formal full-time employment Offer letter with the compensation, terms and conditions, rules, regulations & policies ("Employment Agreement") will be issued and such Employment Agreement shall be subject to and be effective only upon your successful completion of full-time internship, if offered to you, and background verification.

2. If full-time internship is offered to you,

a) You will have an opportunity to do a full-time internship where you would be deployed into a formal training to a business specific skill track, and it will be used as basis towards your allocation to project/roles and terms and conditions governing such training program shall be detailed in that Offer Letter.

b) It is hereby clarified that participation in this full-time internship program shall not deem to constitute you to be an employee of Cognizant and the scope of this full-time internship program does not include any supervisory responsibilities and that there is no agency, tenancy or employer-employee relationship intended or created by reason of this LOI.

c) Cognizant shall carry no worker's compensation insurance or any health or accident insurance under this full-time internship program and not be liable to pay any contributions to applicable statutory payments such as workmen compensation, provident fund nor shall provide any other contribution or benefits which might be expected in an employer-employee relationship.

d) Cognizant will not encourage any claim with regard to compensation or other statutory payments under this LOI and it is here by clarified that participation in this full-time internship program shall not entitle you for any benefits paid or made available to that of Cognizant employees.

3. Upon successful completion of full-time internship program and on completion of background verification, you would be onboarded as a full-time employee under a definitive Employment Agreement, as per business needs.

4. If direct full-time employment is offered to you upon your successful completion of background verification, you would be onboarded as a full-time employee under a definitive Employment Agreement, where you should successfully complete the Cognizant identified training program / on the job training. In the event of unsatisfactory performance during this training, Cognizant reserves rights at its sole discretion to terminate full-time employment.

5. Upon successful completion of full-time internship and joining as full-time employee, or directly joining as full-time employee, your annual total cost to company (CTC) shall be **INR 416,331 /- per annum**. The detailed information on compensation and benefits will be provided to your full-time employment Offer Letter.

6. Cognizant has zero tolerance towards malpractice in any form and reserves all rights with respect to hiring decisions and issue of full-time Offer. This document is only a letter of intent and does not constitute any contractual relationship between you and Cognizant. Cognizant holds all rights to withdraw or cancel this LOI and/or the Internship Offer Letter, Full-time employment Offer Letter due to non-conformance of performance benchmark or moral code of conduct.

Code of Conduct - You shall comply with Cognizant's Core Values and Standards of Business Conduct, located at and incorporated herein by reference <https://www.cognizant.com/india/documents/code-of-etiquette.pdf>.

7. This LOI from Cognizant is valid for **5 calendar days**, from the date of the LOI. Hence, you are requested to accept or decline the LOI within 7 calendar days from the date of the LOI. In case we do not receive any response from you and within the aforementioned time period, this LOI shall stand withdrawn, and will be considered as void. Any extension to the LOI validity will be at the sole discretion of Cognizant. Please note that this LOI is not deemed to be considered as an employment contract or offer letter for purposes of joining Cognizant as an employee. For all onboarding formalities, the definitive Employment Agreement is required.

8. Upon accepting this LOI, you hereby consent for Cognizant Technology Solutions India Private Limited to receive the personal information and/or sensitive personal information ("Personal Information") submitted by you for background verification and share the same with its unaffiliated vendors located in India for conducting mandatory background checks for employment opportunities with Cognizant.

A copy of proof

Our Final-year student (Batch 2022-2026), Sai Srinivas S secured a placement offer as an Associate Analyst at Zifo, Chennai with CTC of Rs 4.7 lakhs.



A copy of proof

Our Final-year student (Batch 2022-2026), Keerthana SL has secured admission in The University of Queensland, Australia for her Masters in Molecular Biology with a scholarship of Rs. 23 lakhs

Our final year student Anusha S and Mayuri Arvind (Batch 2022-2026) has secured admission in University College Dublin, Ireland for her Masters in Biotherapeutics and Biotechnology with a scholarship of Rs. 7 lakhs each.



A copy of proof

Our final year student Krithika V (Batch 2022-2026) has secured admission in RMIT University, Australia for her Masters in Biotechnology with a scholarship of Rs. 12 lakhs



A copy of proof

Our final year student Ferlinsa J (Batch 2022-2026) has secured admission in University of Copenhagen, Denmark for her Masters in Biosolutions.



A copy of proof

	 <i>A copy of proof</i>	
2.	ALUMNI TALK	Our alumni Dr. Pranav Vasanthi Bhathri Narayanan, Research Fellow at University of Birmingham, UK interacted with our 2nd and 3rd year students about his Journey as a Bioengineer: Pros and cons of cross discipline research on 6th April 2026.

	 <i>A copy of proof</i>	
3.	INDUSTRIAL INTERACTION	Resource persons Ms. Roopa and Dr. Vimal from Plant Lipid Pvt Ltd interacted with our 2nd and 3rd year students about a flagship industry sponsored initiative on 7th April 2026.



A copy of proof

4.

PAPER PUBLICATION BY FACULTY

Dr. G. Baskar from the Department of Biotechnology has published a paper on “Enhanced biodiesel production from Annona squamosa seed oil using Ni-doped CaO nanocatalyst: Process reaction kinetics” in the journal Energy and Environment

Energy & Environment

Impact Factor: 4.8 / 5-Year Impact Factor: 4.2Journal

Restricted access

Research article

First published online March 28, 2024

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Enhanced biodiesel production from *Annona squamosa* seed oil using Ni-doped CaO nanocatalyst: Process reaction kinetics

Gurunathan Baskar, Sampath Nithica, and Krishnamurthi TamilarasanView all authors and affiliations

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Abstract

The present research was mainly focused on the production of biodiesel from *Annona squamosa* oil using a synthesized Ni-doped CaO nanocatalyst. The optimization of the transesterification reaction parameters was studied through response surface methodology. The highest biodiesel yield of 99.1% was achieved with the optimized conditions of 7.86% catalyst concentration, 442 RPM, 15.19:1 molar ratio of methanol to oil, reaction temperature of 55.8°C and reaction time of 63.3 min. The results obtained from reaction kinetics study showed a good fit with a first-order kinetic model. The activation energy and R² value were determined to be 53.7 kJ/mol and 0.90, respectively. The synthesized Ni-doped CaO nanocatalyst was characterized using Scanning Electron Microscope with Energy Dispersive X-ray Spectroscopy which confirms the presence of nickel, calcium and oxygen. Also, the average size of the nanocatalyst was found to be 48.79 nm. The Fourier Transform–Infrared Spectroscopy results showed the occurrence of functional groups such as C–H and C = O bonds in the synthesized Ni-doped CaO nanocatalyst. The presence of fatty acid methyl esters in the produced biodiesel was analyzed through Gas Chromatography–Mass Spectrometry analysis. The obtained results from the current study provides the possibility and insights for sustainable biodiesel production and a greener environment.

A copy of proof

Dr. M. Chamundeeswari from the Department of Biotechnology has published a paper on “Rice husk ash-delivered silica nanoparticles as eco-friendly sensitizers for enhanced solar photovoltaic performance” in the Journal of Environmental Nanotechnology

	Journal of Environmental Nanotechnology  Research Article Rice Husk Ash-derived Silica Nanoparticles as Eco-friendly Sensitizers for Enhanced Solar Photovoltaic Performance Chamundeeswari Munusamy^{1*}, Mohanapriya Purushothaman¹, Chitrakala Subramanian¹, Mary Nancy Flora Rayappan² and Ramya Ravi³ ¹Department of Biotechnology, St. Joseph's College of Engineering, OMR, Chennai, TN, India ²Department of Chemical Engineering, Vel Tech High Tech Dr. Rangarajan Dr. Sakunthala Engineering College, Avadi, Chennai, TN, India ³Department of Petrochemical Engineering, JCT College of Engineering and Technology, Coimbatore, TN, India Received: 23.02.2026 Accepted: 04.03.2026 Published: 30.03.2026 *chamundeeswari@gmail.com ABSTRACT Nanostructured silica has attracted considerable attention due to its high surface area, tunable physicochemical properties, and wide applicability in electronic, catalytic, and biomedical systems. In this study, high-purity silica nanoparticles were synthesized from rice husk ash through a thermo-chemical route, providing a value-added strategy for agricultural waste utilization. The novelty of this work lies in the development of rice husk-derived silica nanoparticles as sensitizing materials for photovoltaic applications. Structural and morphological properties of the synthesized material were characterized using X-ray diffraction (XRD), Fourier transform infrared spectroscopy (FT-IR), scanning electron microscopy coupled with energy-dispersive X-ray spectroscopic, transmission electron microscopy (TEM), Brunauer-Emmett-Teller analysis and X-ray fluorescence (XRF) spectroscopy. FT-IR spectra showed characteristic peak at 779 and 1112 cm⁻¹, confirming the formation of an amorphous silica network, while XRD exhibited a broad diffraction peak at 2θ = 21.57°, indicating the amorphous nature of the material. The XRF analysis confirmed ~100% silica purity, and TEM images revealed uniform particles with sizes below 10 nm and mesoporous morphology. Photovoltaic performance evaluation demonstrated enhanced voltage generation under sunlight irradiation compared with green-synthesized carbon nanoparticles and the conventional dye Coomassie Brilliant Blue. These findings suggest that rice husk-derived silica nanoparticles can serve as promising eco-friendly sensitizing materials for sustainable photovoltaic applications. Keywords: Rice husk; Silica nanoparticles; Muffle furnace; Xerogels; Siloxane.	
5.	FACULTY CONFERENCE PRESENTATION	Dr. M. Chamundeeswari from the Department of Biotechnology has won best paper presentation under the Research Scholar category at the IMSTEM 2026, organized by the Department of Science, St. Joseph's College of Engineering, Chennai & RSP Research Hub, Coimbatore during 20-21, March 2026.



SJCE & RSP IMSTEM 2026 - B61

A copy of proof

DEPARTMENT OF CHEMICAL ENGINEERING

S.No.	Title of the Events and Photographs	Details of the Event
6.	HANDS ON TRAINING (PRO SIMULATOR)	The Department of Chemical Engineering, in association with SIM InfoTech, successfully organized a five-day hands-on training workshop on the simulation tool PRO/II Simulation Software for III Year Chemical Engineering students. The workshop was designed to provide practical exposure to industry-relevant simulation techniques, enabling students to model, analyze, and optimize chemical processes using advanced software tools. Expert trainers from SIM InfoTech guided the sessions, ensuring an interactive learning environment with a strong focus on practical application. 
7.	ALUMNI INTERACTION	Mr. R. Prem Kumar, working in the Food Safety Department at the Coca-Cola Company, USA, and Mr. M. Vijay Vignesh Vanan, IT entrepreneur (Batch 2008–2012)—both proud alumni of the department—visited and interacted with students and faculty. During the session, they shared their professional journeys and delivered an engaging alumni interaction, emphasizing the importance of effectively utilizing college life for skill development, internships, and building a strong career

		foundation. They encouraged students to stay curious, proactive, and industry-ready. 
8.	STUDENT ACHIEVEMENT	First-year students achieved a remarkable feat by securing the First Prize at Makethon 3.0, organized by the Smart India Hackathon (SIH) team of the institution. Competing against participants from various years and disciplines, the team proved that determination and creativity transcend experience. The winning team comprised Santhosh R (Mechanical Engineering), Sreemayee R (Chemical Engineering), Lakshmitha C (Chemical Engineering), and Kalaiyaran D (Chemical Engineering)—all first-year students who demonstrated exceptional enthusiasm and problemsolving capabilities early in their academic journey. The team's success was guided by the dedicated mentorship of Dr. T. Amudha, Assistant Professor, whose continuous support, technical

insights, and encouragement played a pivotal role in shaping their winning solution.



Ms. Akshaya S, who secured the **3rd Prize** for her outstanding performance in the **Paper Presentation at the Fifth Regional Conclave (Southern Region)** organized by **The Institution of Engineers (India)**. The event, held on 7th–8th April 2026 at Sathyabama Institute of Science and Technology, focused on the theme “Engineering Resilience: Technology, Environment and Society.” Ms. Akshaya S was recognized for her innovative presentation titled “**Real-Time Bus Occupancy Insights System with Bus Tracking.**”



Akshaya S and Panbarasan T, III Year students of the Department of Chemical Engineering, brought laurels to the institution by securing the **Third Prize** at a prestigious National Level Technical Symposium organized by the IEEE Professional Communication Society at **Sri Sai Ram Engineering College**.



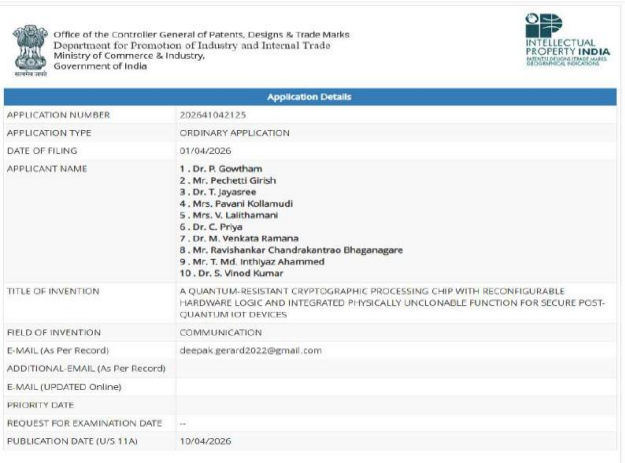
		The first-year girls' team from Chemical Engineering delivered an energetic and captivating performance in the dance competition, securing a prize and earning wide appreciation from the audience and judges alike. 
9.	STAFF ACTIVITY	Dr. N. Venkatesh, Professor and Head of the Department, has added another milestone to his academic achievements by successfully completing two NPTEL certification courses in early 2026. He secured an impressive 82% in the 8-week course “Literature and Coping Skills” (Jan–Mar 2026) and 84% in the 4-week course “Bioelectrochemistry” (Jan–Feb 2026).  Dr. N. Magesh, Assistant Professor, has successfully completed the NPTEL certification course titled “Plastic Waste Management”. He secured the prestigious ELITE grade, reflecting his strong commitment to academic

excellence and continuous learning.



Dr. S. Vinod Kumar, Associate Professor, has published a *book chapter titled “AI-Powered Technologies for Plastic Waste Management and Recycling”* with IGI Global Scientific Publishing.

Dr. S. Vinod Kumar, Associate Professor, has published a patent *titled “A Quantum-Resistant Cryptographic Processing Chip with Reconfigurable Hardware Logic and Integrated Physically Unclonable Function for Secure Post-Quantum IoT Devices.”*



Dr. T. Amudha, Assistant Professor, has published two patents titled “**AI Based Block chain Algorithm Device**” and “**Predictive HR Analytics and AI Enhanced Employee Performance System.**”

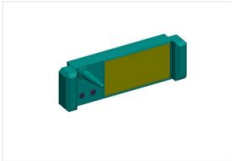
(REPRESENTATION SHEETS)

Name of the applicants: Dr. K.GANESH, Dr. V.KANNAN, Mrs. RAMYA S, Dr. S.DHANASEKARAN, Dr. T. AMUDHA, Dr. VINAY AVASTHI, Dr. B GAYATHRI & Dr. N.JAYAKANTHAN

Design Application no: _____

No of sheets: 7

Sheet no: 1



PERSPECTIVE VIEW

The novelty resides in the shape and configuration of **AI BASED BLOCKCHAIN ALGORITHM DEVICE** as illustrated above.

No claim is made by virtue of this registration in respect of any mechanical or other action of any mechanism whatever or in respect of any mode or principle of construction of the Article.

No claim is made by virtue of this registration to any right to the exclusive use of the words, letters, numbers, or trademarks appearing in the representation.

Dated : 03/10/2025


Dr. T. Amudha

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CERTIFICAT D'ENREGISTREMENT / CERTIFICATE OF REGISTRATION

Date d'enregistrement /
Date of registration
28 JANV / JAN 2026

Titre /
Title
PREDICTIVE HR ANALYTICS AND AI-
ENHANCED EMPLOYEE PERFORMANCE
SYSTEM

Catégorie /
Category
LITTÉRAIRE / LITERARY

Titulaire(s) /
Owner(s)
DR. MOHAMMED QUADIR, MOHIUDDIN,
ASSISTANT PROFESSOR, DEPARTMENT OF
ECONOMICS AND BUSINESS
ADMINISTRATION - DEBA
UNIVERSITY OF TECHNOLOGY AND APPLIED
SCIENCES - IBRI, AD DHAKIRAH-511
OMAN

DR. ROHINI VENKATACHALAM,
PROFESSOR, COMPUTER SCIENCE
CHRIST UNIVERSITY, BANGALORE,
KARNATAKA- 560029,
INDIA

1241628
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Registration number

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Sl. No.	Event with Photo	Description
1	START UP FEST	Date : 16.4.2026 Venue : Leo muthu stadium Nature of Event : Start-up fest Participants : All colleges Objective : • Creating a high-pressure, high-reward environment where founders can pitch directly to Venture Capitalists and Angel Investors to secure funding. • Facilitating "meaningful collisions" between founders, potential co-founders, mentors, and corporate partners to build a support ecosystem. • Providing a massive stage for early-stage companies to demo products, gain media coverage and validate their ideas with real-world user feedback. Outcome : Closed investment rounds, signed Term Sheets, and new pilot programs between start-ups and corporations. • Founders leaving with refined pitches, updated business models, and actionable advice from industry experts



DEPARTMENT OF MECHANICAL ENGINEERING

Sl No	Name of the Activity	Remarks
1		SAE BAJA Teams has bagged 8 trophies and Rs 35,000 cash reward in ATVC Event conducted Monark University Ahmedabad. Hydrogen Edition Vehicle: Overall - All India Rank (AIR) 3 position with Cash award Rs. 10,000/- Business presentation All India Rank (AIR) 2 position with cash award Rs 5000/-, Cost Report All India Rank (AIR) 2 position with cash award Rs 5000/-, Design Evaluation - All India Rank (AIR) 3 position, Aesthetic Vehicle Award
2		➤ Faculty Achievements: ➤ Mr.K.Gnanasekaran, Assistant Professor from the Department of Mechanical Engineering published a research article titled Development of Wear Maps for AA7075/Al₂O₃/Ca₂SiO₄ Hybrid Composites in Transactions of the Indian Institute of Metals a SCIE & Anna University Annexure 1 ,Q2 Journal. ➤ Mr.T.Balasubramanian, Assistant Professor from the

		Department of Mechanical Engineering, has been authored for book “Metrology andMeasurements” published by Quill Tech Publisher, ISBN: 978-93-6786- 952-9. ➤ Mr.K.Gnanasekaran, Assistant Professor from the Department of Mechanical Engineering published a patent under a title of A Method for Producing Self-Lubricating Composite Materials for Tribological Applications application number- 202641031147 A ➤ Dr.M.Ganesh acted as a reviewer for the manuscript titled , "Integration and effectiveness of nano silicon particles on machinability performance of the aluminium alloy hybrid composite " in the journal "Sillicon". ➤ Mr. T. Balasubramanian, Assistant Professor from the Department of Mechanical Engineering published a patent under a title of “ A System and Method for Network Reliability Estimation Using Graph Based Topology Analysis”, Application number - 202641031906. ➤ Mr. K. Gnanasekaran , Assistant Professor from the Department of Mechanical Engineering published a patent under a title of
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		Design and fabrication of solar air heater using doublet pipe channel for boiler applications application number-202641035127.
3		➤ On 09/04/2026, Department of Mechanical Engineering, in association with the IEI Student Chapter, organized a one-day workshop for 2nd and 3rd year students on “Origin to Insight: Graph Plotting Made Easy” Students received hands-on training with practical exposure to Design Expert, enhancing their research and technical competencies. The session was handled by Dr. G.M. Lionous Leo, Associate Professor, Department of Mechanical.
4		➤ On 10/04/2026, Department of Mechanical Engineering, in association with SAE India Student Chapter, organized a one-day workshop for 2nd and 3rd year students on "Hands on Training on Energy Conservation in Refrigeration and Air-Conditioning" Students received energy conservation techniques and hands-on training with practical exposure, enhancing their core technical competencies. ➤ The session was coordinated and handled by Dr. K. Arun, Associate Professor, Department of Mechanical Engineering.

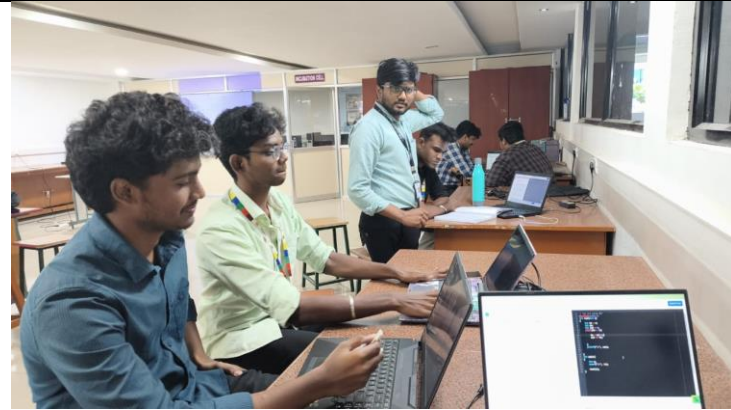
5		➤ On 10/04/2026, Department of Mechanical Engineering, in association with the IEI Student Chapter, organized a one-day workshop for 2nd and 3rd year students on “OPTIMIZATION BY RESPONSE SURFACE METHODOLOGY” ➤ Students received hands-on training with practical exposure to Design Expert, enhancing their research and technical competencies. The session was handled by Dr. M. Ganesh, Assistant Professor, Department of Mechanical.
6	DEPARTMENT OF MECHANICAL ENGINEERING INDUSTRY INSTITUTE INTERACTOIN 4 QUALITY EDUCATION    9 INDUSTRY, INNOVATION AND INFRASTRUCTURE    Mr. T. Balasubramanian and Mr. K. Pravinkumar, Assistant Professors, visited IIITDM Kancheepuram and Crescent Engineering College as CATIA 3DEXPERIENCE in-charges, along with EDS Technologies officials. Outcome: Gained knowledge on server-related information, setup of the center, and overall infrastructure 21/04/2026	Industry Institute Interaction ➤ Mr. T. Balasubramanian and Mr. K. Pravinkumar, Assistant Professors, visited IIITDM Kancheepuram and Crescent Engineering College as CATIA 3DEXPERIENCE in-charges, along with EDS Technologies officials. Outcome: Gained knowledge on server-related information, setup of the center, and overall infrastructure

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Events conducted:

The following events have been conducted during April 2026 at College Level

1. Placement Activities	Report
a) HDP - HUNDRED DAYS PROGRAM	
	Students actively participating in the HDP (Hundred Days Program) through hands-on learning and skill development. They are engaged in practical sessions such as coding, teamwork, and project-based activities in a classroom and lab environment. The program emphasizes continuous learning, discipline, and real-time problem-solving skills. Faculty guidance and peer collaboration play a key role in enhancing students' technical knowledge. Overall, HDP helps students improve their employability and prepares them for industry-ready skills.



2. PLACEMENT EMPOWERMENT PROGRAM

Report

a) TECHNICAL SESSION CONDUCTED BY RENESAS ALTIUM TEAM

	ECE Students from Semiconductor and VLSI Design Centre attended the technical Session on “Renesas QuickConnect Studio: Code Customization for Switch and LED Control” conducted by Renesas-Altium Team on 4th February 2026. During the hands-on demonstration, the Renesas-Altium team illustrated how to configure peripherals and customize code for switch input and LED control using QuickConnect Studio. Students learned how to initialize GPIO pins, write basic control logic, and modify embedded C code to respond to switch inputs. The session also covered debugging techniques and best practices for testing embedded applications. The session was highly informative and beneficial for our students. It provided them with valuable exposure to industry-standard tools and practical knowledge on embedded system programming. The participants gained a better understanding of how software and hardware integration works in real-time applications.
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The speakers further explained simulation and validation methods that help designers identify potential signal integrity issues before manufacturing the PCB. Practical examples were presented to illustrate how careful layout design can improve system performance and reduce design errors. The technical session was informative and beneficial for the students. It enhanced their knowledge of advanced PCB design methodologies and exposed them to professional tools and techniques used in the electronics

	industry. 
b) Design and Verification of High Speed Memory Controllers Workshop	
	The Semiconductor and VLSI Design Center of the Department of Electronics and Communication Engineering organized a two-day workshop on “Design and Verification of High-Speed Memory Controllers” on 20th and 21st April 2026 as part of the Placement Empowerment Program at St. Joseph’s Group of Institutions. The session was led by Mr. Shaik Aleem Ur Rehaman, ASIC Design & Verification Engineer at Microsoft, who provided valuable insights into high-speed memory architectures, design challenges, and industry-oriented verification methodologies. The workshop offered

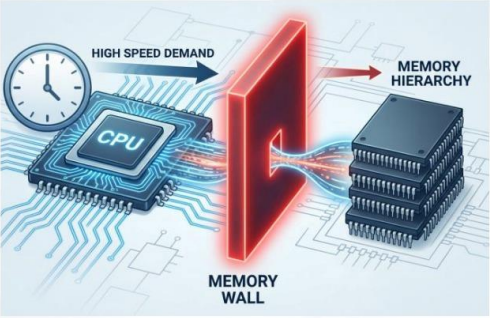


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Placement Empowerment Program

Semiconductor and VLSI Design Center Organizes a Two days workshop on Design and Verification of High-Speed Memory Controllers





Mr. Shaik Aleem Ur Rehaman
ASIC Design & Verification
Engineer at Microsoft



20.4.2026 & 21.4.2026



8:00AM to 2:50PM



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students hands-on exposure and enhanced their understanding of real-time VLSI design practices, thereby equipping them with essential skills required for careers in the semiconductor industry.



Day 1 focused on fundamentals such as memory hierarchy, memory wall, controller design, high-speed interfaces, and key parameters like latency and bandwidth, while Day 2 emphasized verification methodologies including SystemVerilog, UVM, testbench development, debugging, and industry practices. The sessions were highly interactive, offering hands-on insights, discussions, and career guidance. By the end of the workshop, participants gained a clear

	understanding of memory controller architecture, NoC routers, cache systems, and ASIC design and verification workflows, along with exposure to industry-relevant tools and skills, making the program highly informative and beneficial for enhancing technical and career readiness in the VLSI domain.
c) Tech Talk: Semiconductor Ecosystem Powering Modern AI	
	The Semiconductor and VLSI Design Center organized a Tech Talk under the Placement Empowerment Program on “Semiconductor Ecosystem Powering Modern AI” on 8th April 2026, from 12:30 PM to 2:30 PM. The session was delivered by Manickam Saravanan, Senior Engineering Manager at Intel India, who shared his extensive industry expertise with students and faculty members. The talk provided valuable insights into the global semiconductor ecosystem and highlighted the crucial role of chips and processors in enabling modern Artificial Intelligence (AI) applications. The speaker also discussed emerging trends such as edge computing, AI accelerators, and advanced semiconductor technologies, along with


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Placement Empowerment Program
Semiconductor and VLSI Design Center
 Organizes a TechTalk on
Semiconductor Ecosystem Powering Modern AI



Mr. Manickam Saravanan
 Senior Engineering Manager, Intel India

 **8.4.2026**
 **12:30PM to 2:30PM**


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industry expectations and essential skills required for aspiring engineers.

The session was highly interactive, with students actively participating in a Q&A segment to clarify their queries on career opportunities in VLSI and chip design, required technical competencies, and internship prospects in semiconductor companies.

Drawing from real-world experiences, the speaker offered practical perspectives on current challenges and innovations in semiconductor design and manufacturing.

The Tech Talk significantly enhanced students' understanding of the semiconductor domain and its pivotal role in powering modern AI systems, while motivating them to pursue careers in this rapidly evolving field.

Students from the Semiconductor and VLSI Design Centre of the ECE Department were provided with the QuickConnect Beginners Kit, valued at Rs. 7,500/-, by Renesas Semiconductor, Bangalore.

This initiative aims to enhance students' practical knowledge and hands-on experience in embedded systems and VLSI design, thereby bridging the gap between academic learning and industry requirements. The kit will support students in developing innovative projects and strengthening their technical competencies in emerging semiconductor technologies.

3. ALUMNI ACHIEVEMENTS	Report
a) PLACEMENT PREPARATION TALK - 1	
	A Placement Preparatory Talk titled “3C – Cloud Career Connect” was conducted for the 2023–2027 batch to enhance students’ awareness of emerging career opportunities in cloud computing and software engineering. The session featured Mr. Eswara Prasad S, Software Engineer at Presidio Solutions Pvt. Ltd. and an alumnus of the 2018–2022 batch, as the resource person. He shared valuable insights on industry expectations, essential skill development, and effective strategies for placement preparation. The talk proved highly informative and motivating, offering practical guidance to students for strengthening their career readiness and professional growth in the IT sector.

	 The session concluded with a detailed discussion on key outcomes and learning takeaways for the participants. Students gained valuable insights into the recruitment process and role expectations at Presidio Solutions Pvt. Ltd. The resource person highlighted important technical competencies and aptitude areas essential for successful placement preparation. Practical guidance on interview strategies, problem-solving techniques, and industry best practices was also shared. The interaction enhanced students' understanding of current career opportunities and skill requirements in the IT and services sector. Overall, the programme significantly strengthened students' confidence, clarity, and preparedness for future campus recruitment initiatives.
b)PLACEMENT PREPARATION TALK - 2	





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Placement Empowerment Program

Semiconductor and VLSI Design Center

Organizes an Alumni Talk on

Building a Career in Analog and AMS Design:

From Fundamentals to Industry



Mr. Michael Arul Abel J
 ECE BATCH 2021-2025
 AMS Application Engineer
 Cadence Design System, Bangalore


8.4.2026


8:00AM to 9:20AM


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The Semiconductor and VLSI Design Center organized an Alumni Talk under the Placement Empowerment Program on “Building a Career in Analog and AMS Design: From Fundamentals to Industry” on 8th April 2026, from 8:00 AM to 9:20 AM. The session was delivered by Mr. Michael Arul Abel J, an alumnus (ECE Batch 2021–2025) currently working as an AMS Application Engineer at Cadence Design Systems, Bangalore. It was attended by students and faculty members from the Electronics and Communication Engineering domain, with a focus on guiding participants toward careers in Analog and Mixed Signal (AMS) design through strong foundational knowledge and practical exposure.



The speaker provided a clear roadmap to enter the AMS domain, highlighting the importance of analog fundamentals, industry-relevant tools such as Cadence, and hands-on design practices. He also shared insights into real-world challenges, project experiences, and industry expectations, emphasizing continuous learning, skill development, and building a strong technical portfolio. The session concluded with an interactive Q&A, where students clarified their queries on career paths, required skill sets, internships, and transitioning from

	academics to industry, making the talk highly informative and motivating for aspiring VLSI engineers.
c) PLACEMENT PREPARATION TALK - 3	
	The alumni excellence of ECE department in global business services. It features Mr. Guhan Mohan, an alumnus of the ECE Department (2004–2008 batch) and a University Rank Holder. He currently serves as Head of Programs and Strategic Initiatives, Global Business Services at MassMutual, Boston, USA. His leadership contributes to global talent expansion and enterprise-wide strategic outcomes. The poster showcases how St. Joseph's alumni progress from learning to leading on an international stage. It reflects the institution's strong academic foundation and successful alumni network.
4. STAFF ACHIEVEMENTS	Report
a) Activity 1	

	Our faculty member, Dr. D. Lakshmi, Associate Professor, was invited as a Guest Speaker and successfully delivered an online presentation on the topic “AI-Driven Clinical Diagnostics using Deep CNNs for Multi-Modal Medical Images” at the 4th International Conference on Recent Trends in Materials Science & Devices (ICRTMD-2026). The conference was hosted by Janata Vidya Mandir Ganpati Rai Rasiwasia College, Charkhi Dadri, Haryana, India. 
b)Activity 2	
	The ECE Department faculty have been recognized for their outstanding contributions with a total reward of ₹13,32,075, awarded for achievements in research publications, academic excellence, and professional contributions. This recognition reflects the department’s strong emphasis on research, innovation, and academic quality. Such accolades

serve as a motivation for faculty members to continue their scholarly pursuits and strive for higher standards. Overall, these achievements significantly enhance the reputation and academic strength of the ECE Department.



c)Activity 3



St. Joseph's College of Engineering proudly celebrates the remarkable achievements of its faculty members, with a total of ₹1.94 crore awarded as staff rewards for the academic year 2025–26. This significant recognition highlights the institution's strong commitment to excellence in teaching, research, and professional contributions. The initiative not only honors the dedication and hard work of the faculty but also fosters a culture of innovation and continuous improvement. With the theme "Celebrating Achievements,

	Inspiring Success,” the college reaffirms its mission to motivate educators and strengthen academic excellence, thereby contributing to the overall growth and reputation of the institution.
d)Activity 4	
	Dr. P. Latha, Associate Professor, Department of ECE, successfully completed a 3-day Faculty Development Program on “Wireless System Design using FPGAs,” organized jointly by MathWorks and Space Applications Centre, ISRO. The program was held at MathWorks India Pvt. Ltd., Bangalore, from 23rd to 25th February 2026. It offered a valuable opportunity to explore practical aspects of wireless system development using advanced tools and technologies.





During the program, she gained hands-on experience in end-to-end wireless system design using MATLAB and Simulink, physical layer modeling, HDL code generation, FPGA implementation, and FPGA-in-the-loop verification. The sessions blended theory with real-time applications, guided by industry experts. The training enhanced her practical knowledge in FPGA-based wireless systems.

e)Activity 5	
	Dr. P. Latha successfully completed a 3-day Faculty Development Program on “Wireless System Design using FPGAs,” organized by MathWorks and Space Applications Centre (ISRO). The program, held at MathWorks India Pvt. Ltd., Bangalore, offered practical exposure to wireless system design using MATLAB and Simulink, along with FPGA implementation techniques. The sessions provided valuable hands-on learning and insights into current advancements in wireless communication technologies.
f)Activity 6	
	Latha P, Associate Professor, Department of ECE, has added two notable research achievements to her credit. She presented an IEEE conference paper titled “GPU-Powered Sentiment Classification with VGG19 and RF+XGB for Improved Product Recommendations,” focusing on AI-driven product analysis. She has also published a research article, “Smart and Efficient Waste Management through Wireless IoT-Enabled Deep Learning,” in Scientific Reports, a Q1 journal with a 5-year impact factor of 4.3. These accomplishments reflect her active involvement in advanced research and innovation.
g)Activity 7	

	GD.Vignesh Gd has received a Reviewer Certificate from Springer Nature in recognition of reviewing two manuscripts in the field of Telecommunication Systems. This achievement highlights his subject knowledge and contribution to academic research. We congratulate him on this recognition and appreciate his dedication towards scholarly activities and professional excellence.
6) Publications	
<u>Journal Publication</u> 1) Vignesh G D and A M Balamurugan, Assistant Professors has presented a paper titled “Performance Analysis of Adaptive Switching Spectrum Sensing (ASSS) With WOA Technique for Improved PUDetection Under Segmentation-Based V-I Framework in CR-VANETs” in International Conference of communication techniques . 2) Vignesh GD, AM Balamurugan, P prasanth , U prabu Kanth Assistant Professors has presented a paper titled “Hybrid DDAC: Dual-Domain Attention and Transformer Network for Adaptive UAV Image Restoration” in 5Tth International Conference on communication, computing and Electronics Systems(ICCCES) 3) Vignesh GD, Ramachandra Reddy K, Mothish Kumar R, Mugilan RJ Assistant Professors has presented a paper titled “A Dual-Path YOLO26 Framework with Micro-Segmentation and Displacement-Aware Correlation for Automated Lumbar Vertebral Analysis and Early Spondylolisthesis Detection” in 2026 International Conference on AI-Drive Smart Systems and Ubiquitous Computing (ICAUC)	

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Photographs Captured During Events	Corresponding remarks (Minimum 300 words)
1.	STUDENT COMPETITION  <i>PIC: EVENT PHOTO</i>	Hearty congratulations to the students of Team ZIRCONS for their outstanding achievement in the National Level event “SIEP E-Bike Challenge 2026”, organized by the Imperial Society of Innovative Engineers (ISIEINDIA) from 3rd to 7th April 2026 at Galgotias University, Greater Noida (UP). The team secured the Future Award along with a cash prize of ₹5,000 and the People’s Choice Award with a cash prize of ₹5,000, showcasing exceptional innovation, teamwork, and technical excellence.

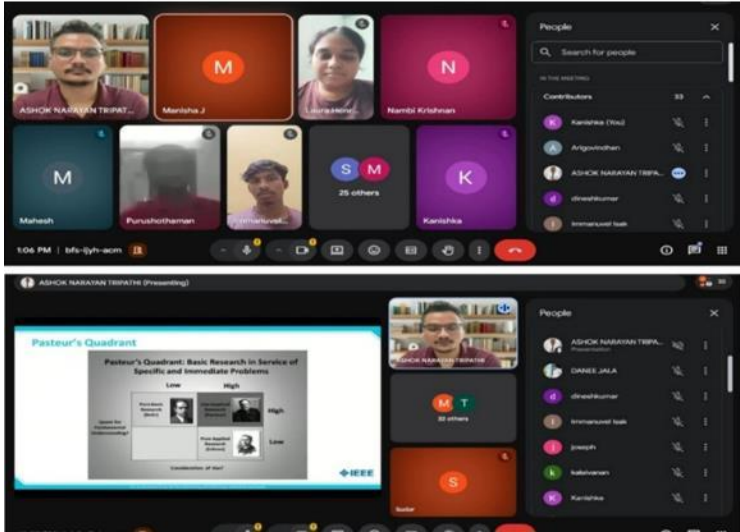
2.	Hackathon Guidance  <i>PIC: EVENT PHOTO</i>	A Hackathon Expert Guidance Session was organized for the hackathon teams of the EEE Department, featuring Mr. Janarthanan Selvaraj, Technology Manager and AI Specialist at Atos, as the resource person on 29th April 2026 (12:00 PM to 3:00 PM). The session offered valuable insights into effective hackathon strategies, innovation techniques, and real-world problem-solving approaches. He shared his expertise in AI, cloud technologies, and industry-oriented project development. The hackathon teams were guided on identifying impactful problem statements and developing scalable solutions using modern tools. Emphasis was placed on teamwork, time management, and delivering compelling presentations. He also explained the expectations of judges and methods to stand out in competitive hackathons. The session inspired the hackathon teams to actively participate in national-level competitions. It was highly interactive and enriched the teams with industry-relevant knowledge. Overall, the session motivated the hackathon teams of the EEE Department to innovate and excel. Around 45 students participated in this session.
3.	IEEE activities	The “Classroom to Corporate: IAS Transformation Series” was conducted on April 9, 2026, from 6:00 PM to 7:00 PM, focusing on the theme “Rapid Digital Transformation with Low-Code Applications.” The session highlighted the increasing importance of low-code platforms in modern industries and their role in accelerating development processes. Ms. Shamrutha J A, Associate Technical Consultant at WNS Vuram and Secretary of IEEE SJCE SBC 2025, served as the distinguished speaker. She shared valuable insights on how low-code technologies reduce coding complexity, improve efficiency, and enable faster deployment of solutions. Real-world use cases were discussed to demonstrate how organizations leverage these platforms to

	 PIC: EVENT POSTER & PHOTO	streamline workflows and drive innovation. The session also emphasized bridging the gap between academic learning and industry expectations through practical skill development.
4.	IEEE activities  PIC: EVENT POSTER & PHOTO	The “Classroom to Corporate: IAS Transformation Series” was held on April 10, 2026, from 6:00 PM to 7:00 PM. The session, titled “From Classroom Concepts to Live Electrical Design Solutions,” focused on connecting academic learning with real-world practices. It highlighted the application of core electrical engineering concepts in live industrial projects. Mr. Alex Amalraj, Electrical Design Engineer based in Bahrain, served as the distinguished speaker. He shared valuable insights on industry expectations, design workflows, and practical challenges. The session emphasized design thinking, problem-solving skills, and adapting to professional standards. An interactive Q&A session enabled students to explore career pathways and gain deeper industry understanding.

5.	IEEE activities  <i>PIC: EVENT POSTER & PHOTO</i>	The “Classroom to Corporate: IAS Transformation Series” was conducted on April 10, 2026, from 7:00 PM to 8:00 PM, on the topic “Control & Protection in HVDC Systems: From Concepts to Real-Time Applications.” The session focused on bridging the gap between academic knowledge and practical implementation in modern power systems. It highlighted how theoretical concepts are translated into real-time hardware solutions to ensure system stability and safety. Ms. Aiswarya T S, Hardware Design Engineer, Hitachy Energy ,the distinguished speaker, shared insights from her role as a Control & Protection Hardware Design Engineer in HVDC systems. She explained key workflows such as control panel design, post-manufacturing inspection, and on-site system integration. The session emphasized the importance of control and protection in regulating power flow, preventing failures, and maintaining system reliability.
6.	IEEE activities	The IEEE St. Joseph’s College of Engineering Student Branch (SBC60101), in association with the IEEE Women in Engineering Affinity Group organized an online session titled “Women in Engineering: Thriving Without Burnout” on April 10, 2026, via Google Meet. The session was handled by Ms. Harini N, an alumna (2021–25 Batch), currently working as a Content Writer and Social Media Strategist at TIC. She shared valuable insights on managing stress, preventing burnout, and maintaining a healthy work-life balance in engineering careers. The session highlighted the importance of mental health awareness and recognizing early signs of stress. Practical techniques such as physical activity, emotional awareness, and personal comfort strategies were discussed. The session was highly interactive, encouraging active participation from

	PIC: EVENT POSTER	students. Overall, it inspired participants to prioritize both personal well-being and professional growth.
7.	IEEE activities	The IEEE Photonics Society (SBC60101L) successfully organized an online quiz event titled “LumenX” on April 12, 2026, via Google Meet. The event was designed as an engaging and fast-paced quiz centered on photonics and light-based technologies. It aimed to challenge participants’ knowledge while introducing them to emerging concepts and real-world applications in the field. The session offered valuable insights into lesser-explored domains of photonics and its relevance in modern engineering and research. Participants explored topics such as optical communication, laser technology, fiber optics, and practical photonics applications. The quiz format encouraged active participation, quick thinking, and healthy competition among students. Overall, the event also highlighted potential career and research opportunities in the field of photonics.

	PIC: EVENT PHOTO	
8.	IEEE activities  PIC: EVENT POSTER	The “Cover to Cover – IEEE Spectrum Readings” session was conducted on the occasion of Education Week on April 13, 2026 from 01:00 PM to 02:00 PM, on the topic “Safe and Efficient Transport System using PLC & SCADA.” The session was delivered by Ms. Iniyasri R K (EEE), who explained the fundamentals of PLC and SCADA systems and their role in improving transportation safety, reducing human error, and enabling real-time monitoring. It highlighted the application of automation in traffic and railway management for efficient system operation. The session, attended by 20 participants from 2nd, 3rd, and Final Year, provided valuable insights into industrial automation in smart transportation and strengthened understanding of real-time control techniques.

9.	IEEE activities  <i>PIC: EVENT PHOTO</i>	A technical session titled “The Energy Barrier” was conducted on April 11, 2026, as part of IEEE Education Society Week, from 12:00 PM to 1:00 PM via Google Meet. The session focused on “Role of Dielectric Materials in Emerging Technologies,” providing insights into their properties and significance in modern electrical and electronic systems. The keynote speaker, Dr. Ashok Narayan Tripathi, High Voltage Design Engineer at GE Healthcare, shared his industry expertise and practical knowledge. He explained dielectric behaviour, insulation challenges, and their applications in high-voltage engineering, emphasizing efficiency, reliability, and safety. The session also highlighted recent advancements in insulation materials and successfully bridged the gap between academic concepts and real-world applications.
10.	IEEE activities	A technical session titled “Dielectra” was conducted on April 12, 2026, from 7:00 PM to 8:00 PM as part of IEEE Education Society Week, focusing on “Diving Deep into DEIS: Opportunities, Scope & Growth.” The session aimed to provide insights into the field of dielectrics and electrical insulation, along with emerging career opportunities and advancements. The keynote speaker, Mr. Gaurav Pathak, Communications Lead & R10 Coordinator, IEEE DEIS WIE 2026, and IEEE Impact Creator & Brand Ambassador, shared his expertise on the DEIS community and its global significance. He highlighted networking and mentorship opportunities within IEEE, innovation in insulation technologies,



PIC: EVENT PHOTO

and exposure to global standards and research. The event witnessed active student participation, making it an engaging and informative session.

11.

IEEE activities



PIC: EVENT POSTER & PHOTO

A webinar titled “SMARTv2X” was conducted on 13th April 2026 as part of the IEEE Education Week celebrations, focusing on Machine Learning for Vehicular Communication. The session was delivered by Dr. Aniruddha Chandra, Associate Professor, Department of Electronics and Communication Engineering, National Institute of Technology (NIT) Durgapur. It aimed to provide insights into the role of machine learning in vehicular communication systems and its applications in smart and connected vehicles. The speaker explained fundamental concepts and their relevance in enabling intelligent transportation systems and V2X communication, while also highlighting future advancements. The webinar witnessed active participation from students and enthusiasts, fostering discussions on emerging trends, real-world applications, and future opportunities in the field.

12.

IEEE activities



PIC: EVENT POSTER & PHOTO

A webinar titled “CMOS Scaling and AI-Driven Solid-State Circuit Innovations” was conducted on April 22, 2026, from 2:00 PM to 3:00 PM. The session was led by Mr. Samuel Tensingh, a renowned expert in VLSI design, who addressed key topics such as IoT in healthcare, agriculture, and smart cities. He also discussed emerging opportunities in the semiconductor industry and advancements in VLSI circuit design. The webinar witnessed the active participation of 30 attendees, making it an engaging learning experience. An interactive Q&A session further enhanced understanding and provided valuable insights to the participants.

13.

IEEE activities



A photo session titled “PES Frames” was conducted on 24th April 2026 from 6:00 PM to 7:00 PM as part of the IEEE PES Day 2026 celebrations. The event was one among several activities held from 20th April to 26th April 2026, including Green Grid Expo, PowerLab, Photo Contest, and Quiz. The session saw enthusiastic participation from 12 IEEE SB PES Day 2026 Ambassadors, capturing the spirit of sustainability, innovation, and the future of energy through creative photographs. The photo setup and backdrop were thoughtfully designed to reflect the vision of IEEE PES, with participants engaging in both individual and group sessions showcasing unity and leadership. Overall, “PES Frames” served as a vibrant and memorable

	<i>PIC: EVENT POSTER & PHOTO</i>	initiative that enhanced member engagement and celebrated the essence of IEEE PES Day 2026.
14.	CLUB Activities  <i>PIC: EVENT POSTER</i>	Placement training session was organized by Bright Byte Coders Club titled "Your Roadmap to Presidio: Skills, Roles & Expectations." on 05.04.2026 from 11:00 AM to 12:30 PM. The session was delivered by Santhosh Kannan S P, Associate Engineer at Presidio (2021–2025 batch ADS). It was organized exclusively for 3rd year EEE students to provide industry exposure and career guidance. The speaker shared insights on company operations and recruitment processes. Students gained valuable knowledge on career opportunities and required skill sets in the industry. The session was highly informative and motivated students to prepare for future career prospects.

15.	Faculty Recognition	1. Dr M Venmathi, Associate Professor, has acted as reviewer for 2 papers in reputed journal “Scientific Reports” 2. Dr V Krishnakumar, Associate Professor, has acted as reviewer for 2 papers in reputed journals “Energy and AI” and “Next Energy”.
16.	Industry Institute Interaction  <i>Pic:Event Photo</i>	Dr. T.V. Narmadha, Professor, Dr. T. Babu, Associate Professor & Mr. S.S. Harish, Assistant Professor visited Madras Institute of Technology, campus and met Dr. Sabitha Ramakrishnan, Nodal officer, Siemens Centre of Excellence. Outcomes of the visit: • Gained insight to setup Center of Excellence Lab
17.	Faculty Publications	1. Dr V Krishnakumar, Associate Professor, has published a paper titled "A new multistep voltage generator using quartet of isolated DC sources with diminished power" in the journal Automatika 2. Mr H Prasad, Assistant Professor has published a book titled “AI for Electrical Engineers” 3. Mr H Umesh Prabhu, Assistant Professor has published a book titled “Machine Learning: Concepts, Algorithms and Applications”. 4. Mr. H Umesh Prabhu, Assistant Professor published a patent titled “AI-Based continuous assessment system using chatgpt and machine learning for student knowledge enhancement in higher education”.

18.	PLACEMENT DETAILS FOR THE MONTH OF APRIL 2026	2022-2026 Batch No of students placed = 126 Students Total No of Offers = 194 Offers Total No of Students (UG) = 171 Total No of Students Sports Causals (UG) = 01 Total No of Eligible Students (UG) = 147 (All Clear) % of students Placed (UG) = $126/171 = 74.12\%$ No of students having single offers = 74 No of students having Double offers = 41 No of students having Triple offers = 6 No of students having 4 offers = 5
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DEPARTMENT OF SCIENCE

Sl.No.	Events	Remarks
1	Collabarative Quality initiatives with other institutions	1. Dr. N. Punitha received association life membership from “Indian Society of Analytical Scientists (ISAS) – Delhi Chapter”, on 08-04-26. 2. The Department of Science has signed an Memorandum of Understanding with Institute of Public Enterprise (IPE), Hyderabad, on 10-04-26. 3. Dr. G. Senthilkumar received SAS Young Research Fellow Membership from “Scholars Academic and Scientific Society”, on 14-04-26. 4. Dr. P. Saravanan had acted as External Examiner for Ph.D. Viva-voce examination at PG & Research Department of Chemistry, Poompuhar College, Melaiyur on 27-04-26.
2	Guest Lecture	1. A faculty insight session on Exploring the MoU and Industry opportunities has been arranged by the Department of Science on 10-04-2026. Dr. M. Karthik, Professor and Program Head, IPE, Hyderabad had delivered a lecture on “Impact of Globalization” and Dr. Muzammil Ahmad Baba, Sr. Asst. Professor, IPE, Hyderabad delivered a lecture on “Strategic Digital Marketing”.
3	FDP/Workshop/Conference	<i>Invited Talk & Chair Person</i> 1. Dr. P. Saravanan had delivered an invited talk at the “4th International Conference on Recent Trends in Materials Science & Devices- 2026” held between 06-04-26 to 08-04-26, conducted by “JVMGRR College, Charkhi Dadri, Haryana”. He also acted as Session chair person for the conference.
4	Publications(only published) details	<i>Journal Publications:</i>

		1. Dr. P. Saravanan published a research article titled “Materials Engineering of Elastomer–Metal Interfaces Under Corrosion, Wear, and Adhesion Stressors ” in the Journal “Journal of Materials Engineering and Performance”. Doi: https://doi.org/10.1007/s11837-026-08273-x 2. Dr. P. Saravanan published a research article titled “Effect of Si/Zr Ratio on the Catalytic Behavior of Pt-Cu/Zr-SBA-15 in Continuous Methane Dry Reforming ” in the journal “RSC Advances” Doi: https://doi.org/10.1039/D6RA02279K 3. Dr. K. Jayamoorthy published a research article titled “SiO₂-functionalized halobenzimidazole: Photophysical modulation, spectroscopic insights, and enhanced anti-inflammatory and anti-diabetic activities” in the journal “Phosphorus, Sulfur, and Silicon and the Related Elements”. Doi: https://doi.org/10.1080/10426507.2026.2648176 4. Dr. P. Krishnan published a research article titled “Anti-corrosion Investigation of N-tert-Amylacrylamide Based Hydrogels” in the journal “Polymer” Doi: https://doi.org/10.1016/j.polymer.2026.130037 Book Chapters 1. Dr. S. Rama published a book chapter titled “Harnessing Electrical Optimization for Scalable and Efficient Fault Detection in Embedded and Power Systems” - in the book “Next-Generation Electrical Engineering: Power Quality Enhancement, E-Vehicle Integration and Future Grids” by Skyline Global Publications. Reviewers: 1. Dr. S. Suresh - Applied Nanomaterials , soil and Sediment Contamination 2. Dr. N.R. Rajagopalan – Indian Chemical Engineer 3. Dr. K. Jayamoorthy – Journal of Composite Materials ,Chemistry Select - Materials Science in Semiconductor Processing Journal of Oleo Science – Discover Molecules - Surface Reviews and Letters – Current Organic Chemistry - Chemical Physics Impact - Mini-Reviews in Organic Chemistry (3 articles) - Journal of Molecular Structure (2 articles) - Soil and Sediment Contamination. 4. Dr. P. Krishnan - Current Nanoscience - Journal of Materials Science: Materials in Electronics 5. Dr. Dr. B. Subash - Journal of Current Scientific Research (2 articles), Materials
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		Science in Semiconductor Processing, Current Analytical Chemistry 6. Dr. K. Dhanaraj - Discover Nano 7. Dr. A. Dhivya - ACS Publication Reviews
5	Other activities(if any)	The Department of Science has conducted the following NGO activities. 1. I ECE A – St. Vincent Anbu Illam, Oragadam – on 02-04-2026 2. I ECE B – Manasu , Thirusulam – on 02-04-2026 3. I ECE C – Paradise Home, Muttukkadu – on 02-04-2026 4. I AML A - Mahimai Illam, Chengalpattu – on 09-04-2026 5. I AML B – Paradise Home, Muttukkadu – on 09-04-2026 6. I CYBER SECURITY – Little Hearts, Panaiyur – on 09-04-2026 7. I CHEMICAL – John D Britto Old Age Home, Kovalam – on 09-04-2026